

Amendments to the Specification:

Please replace the title on page 1 with the following amended title:

A MULTI-CAMERA SYSTEM FOR IMPLEMENTING REMOTE-DIGITAL SLOW SHUTTER VIDEO PROCESSING USING SHARED VIDEO MEMORY

Please replace the paragraph on page 15, line 1 through page 16, line 8, with the following amended paragraph:

Referring to Fig. 3, before describing the signaling and circuitry of the present~~ing~~ invention, a conventional video camera 42-1 will be described. Fig. 3 is a general block diagram of a conventional video camera 42-1 suitable for use with the digital video memory of the present invention. An image sensor 92 receives an image and outputs analog pixel information representing the image on lead 94. An analog-to-digital (A/D) converter 96 converts the analog pixel information to digital image data that is supplied on lead 98 to a refresh memory 100. The refresh memory 100 stores digital image data representing one video field or frame, and provides image data of sufficient visual quality during slow shutter mode. Typically the refresh memory is a dual-port random access memory (RAM). A slow shutter circuit 102 monitors the signal from the image sensor 92 and generates write addresses on lead 104 at which to store the incoming image data, and read addresses on lead 106 from which to read the pixel data for output. Because the image data in the refresh memory 100 is not updated as frequently as 1/60th of a second in slow shutter

mode, the existing image data in the refresh memory 100 is output on lead 108 every 1/60th of a second. A digital-to-analog converter 110 receives the digital image data on lead 108, and converts the digital image data to an analog video signal on lead 113. A synchronization circuit 114 provides different sets of synchronization information on leads 115, 116 and 117 to the image sensor 92, the slow shutter circuit 102, and the refresh memory 100, respectively. The synchronization information on lead 117 includes an identification of the current horizontal line being scanned. The synchronization circuit 114 supplies vertical and horizontal synch pulses on lead 118 to a summer 119. The summer 119 combines the vertical and horizontal synch pulses on lead 118 to the analog video signal on lead 113 to generate the composite video signal on lead 112. In one embodiment, lead 112 is a coaxial cable.